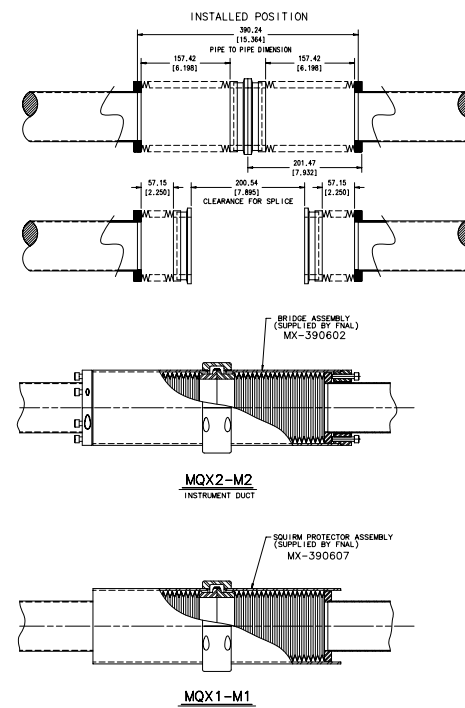
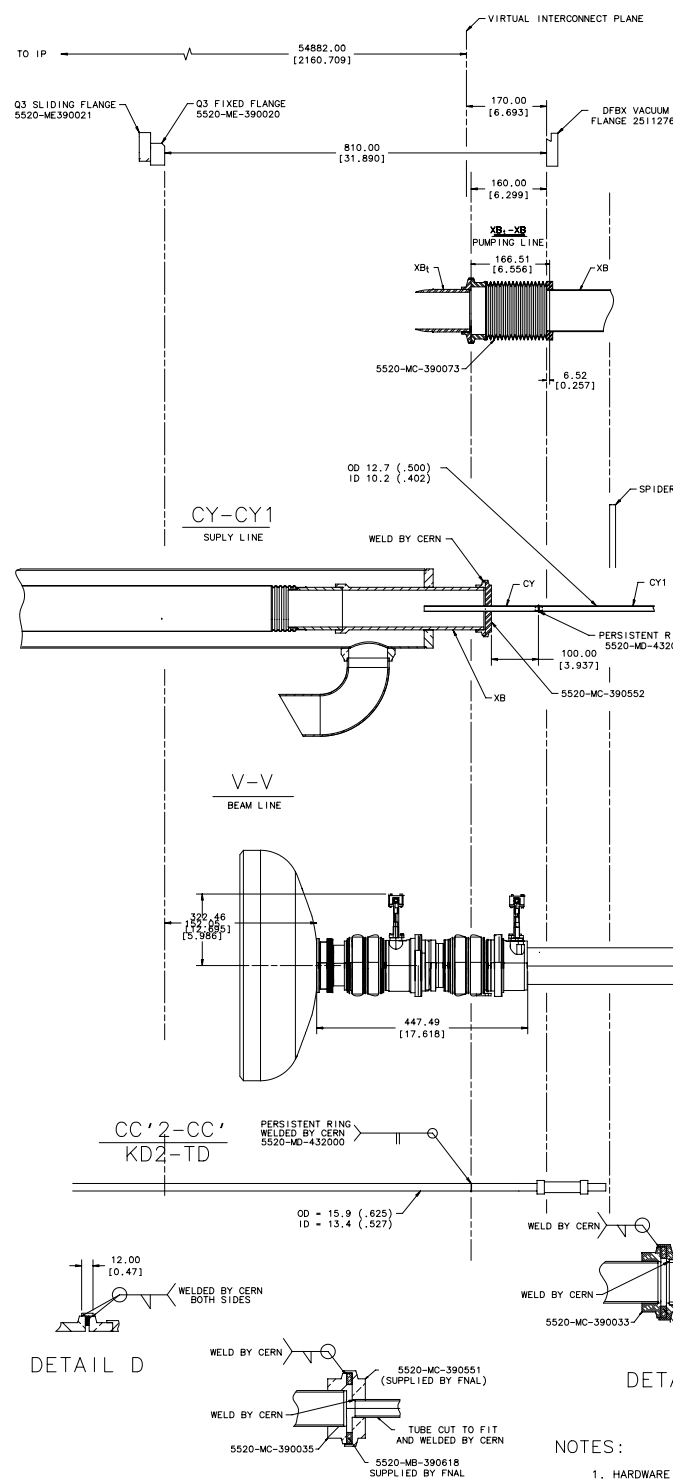
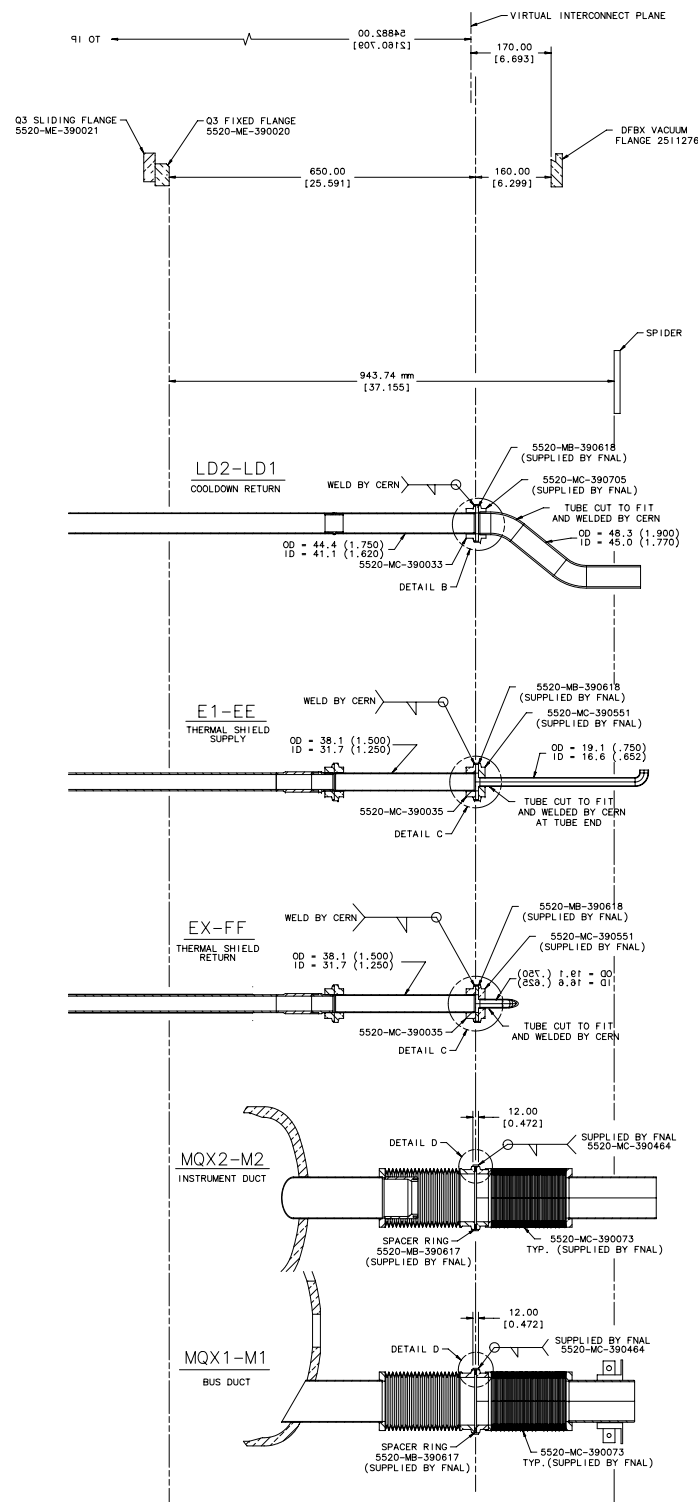
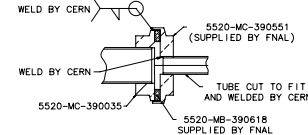


REV.	DESCRIPTION	DATE	APP.	DATE
	E.R #XXXX			



DETAIL D



DETAIL B

NOTES:

1. HARDWARE IS SHOWN IN INSTALLED CONFIGURATION

MODIFICATIONS FOR COVERSION FROM SHIPPING CONFIGURATION ARE COVERED IN LBNL PREPARATION AND INSTALLATION PROCEDURE.

ITEM	PART NO.	DESCRIPTION OR SIZE	QTY.
PARTS LIST			
1	XX	XX	XX
2	XX	XX	XX
3	XX	XX	XX
4	XX	XX	XX
5	XX	XX	XX
6	XX	XX	XX
7	XX	XX	XX
8	XX	XX	XX
9	XX	XX	XX
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16	XX	XX	XX
17	XX	XX	XX
18	XX	XX	XX
19	XX	XX	XX
20	XX	XX	XX
21	XX	XX	XX
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27	XX	XX	XX
28	XX	XX	XX
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147	XX	XX	XX
148	XX	XX	XX
149	XX	XX	XX
150	XX	XX	XX

FERMILAB NATIONAL ACCELERATOR LABORATORY

UNITED STATES DEPARTMENT OF ENERGY

LHC INNER TRIPLET INTERCONNECT

DFBXB-Q3 DETAILED LAYOUT

SCALE: NONE FILMED DRAWING NUMBER: 5525-ME-432016 USER NAME: REV.